

DOCKET NO: 250327US-2S

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
SHINICHI YOSHIOKA : EXAMINER: FLORES, LEON
SERIAL NO: 10/798,403 :
FILED: MARCH 12, 2004 : GROUP ART UNIT: 2611
FOR: SEMICONDUCTOR INTEGRATED :
CIRCUIT DEVICE AND METHOD OF
TESTING THE SAME

SUPPLEMENTAL AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Supplemental to the Amendment filed January 2, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 12 of this paper.